SFXC: The current (e-)VLBI correlator at JIVE

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Collaborators

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- Bob Campbell
- Bob Eldering
- Des Small
- Dmitry Duev
- Harro Verkouter
- Huib Jan van Langevelde, JIVE/Leiden University
- Wouter Vlemmings, Chalmers
- Franz Kirsten, University of Bonn
- Sergei Pogrebenko



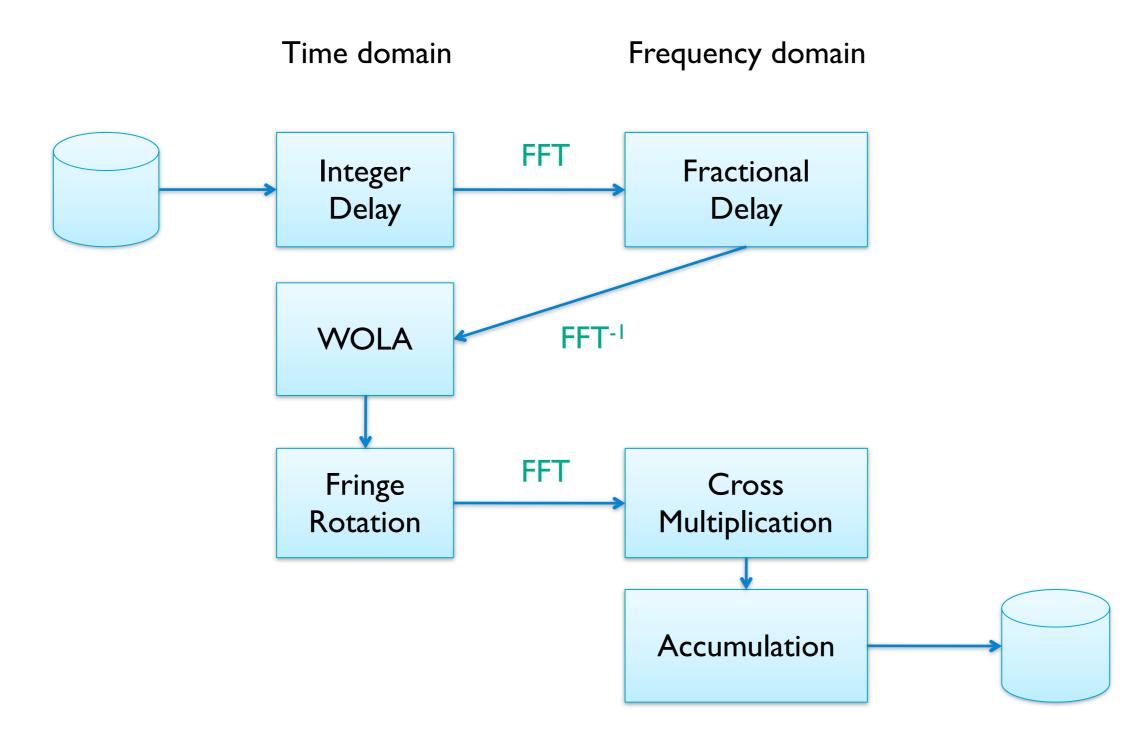
Netherlands Organisation for Scientific Research

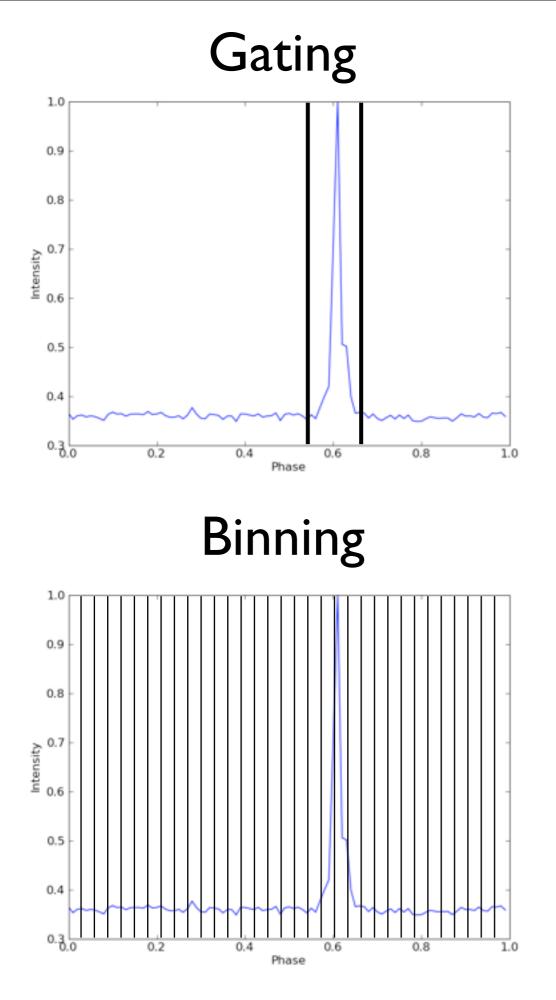


SFXC Features

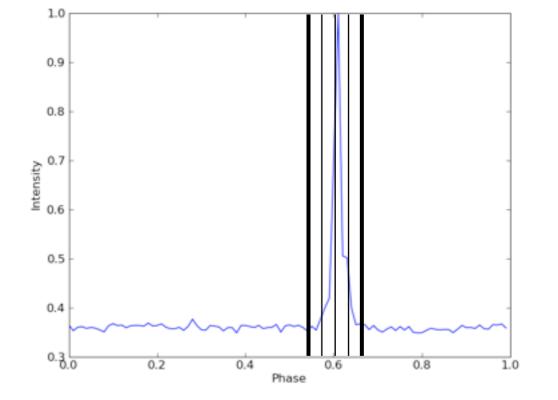
- FX software correlator
- Data formats: Mark4, VLBA, Mark5B, VDIF
- Delay model: CALCI0 (same as Mark4@JIVE and UniBoard), or external
- WOLA: Hann, Hamming, Cosine, Rectangular
- VEX driven, with JSON configuration file
- Implemented using MPI
- Optionally uses commercial Intel IPP library

SFXC Algorithm



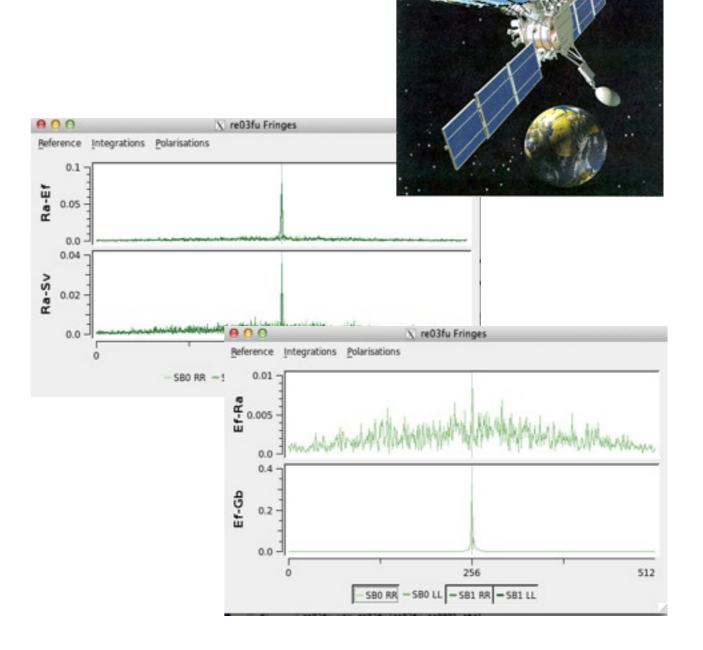


Gating + Binning

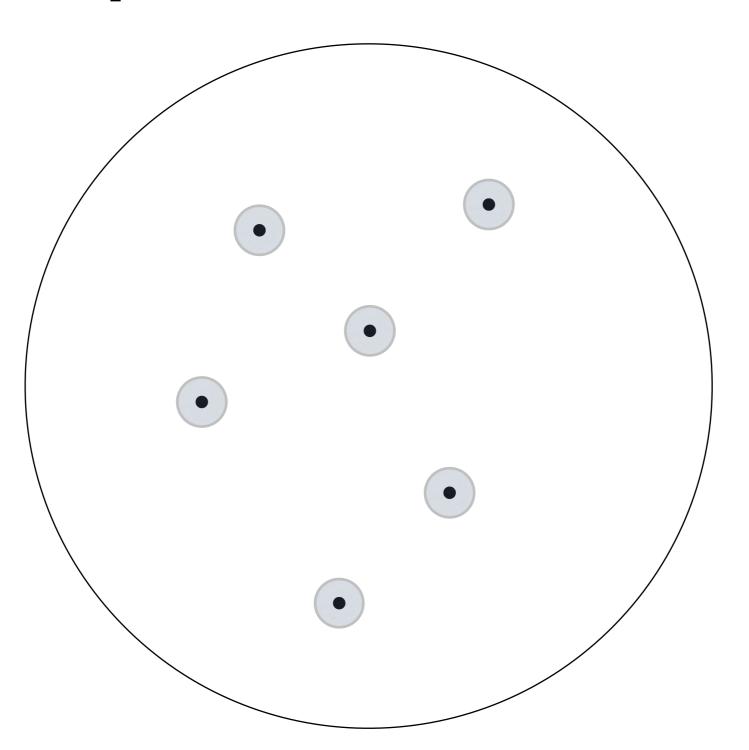


Space Science

- PRIDE, Space VLBI
- "near-field" model
- space craft model
- "negative" delays
- large delays

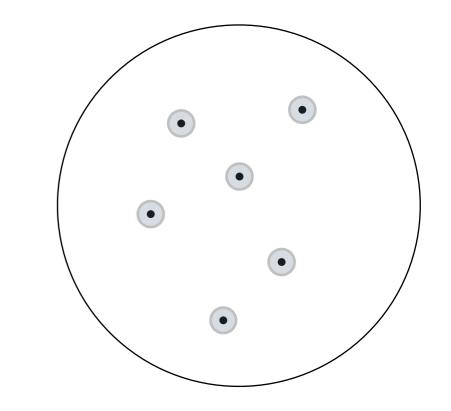


Multiple Phase Centers



Multiple Phase Centers

- 30% slowdown with reasonable S/N loss
- Small additional overhead per phase center
- Model evaluated for each phase center
- No WOLA (yet)



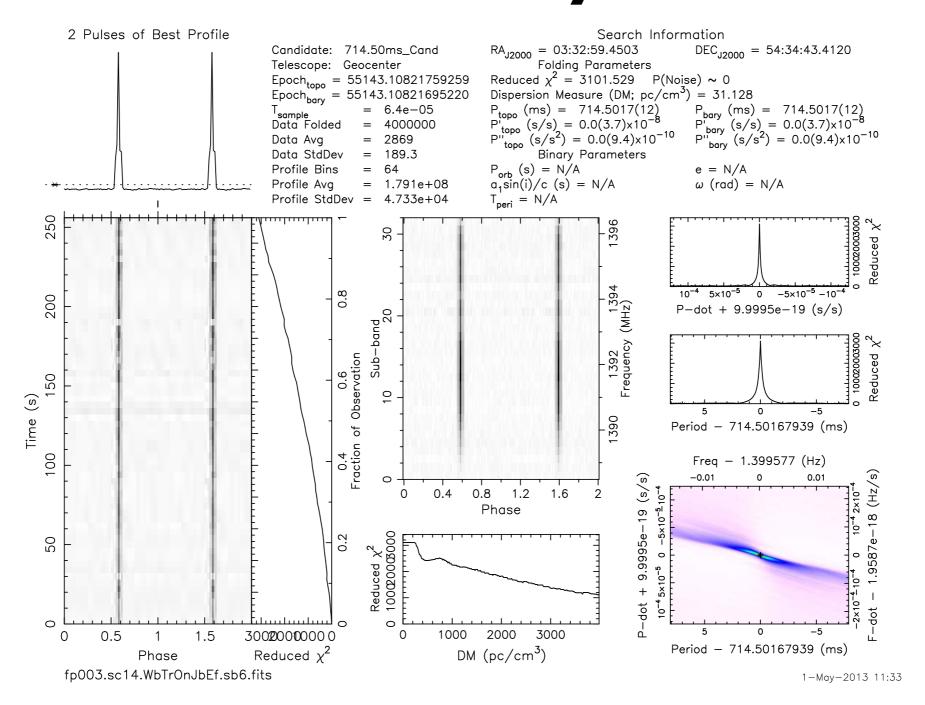
Phased Array Mode

- I. Apply AIPS calibration during delay compensation
- 2. Sum stations
- 3. Integrate total power per frequency bin
- 4. Resample
- 5. Convert to PSR FITS



Can be used as imput for standard Pulsar tools

Phased Array Mode



Pulse profile for B0329+54 summing Ef, Jb, On, Tr and Wb

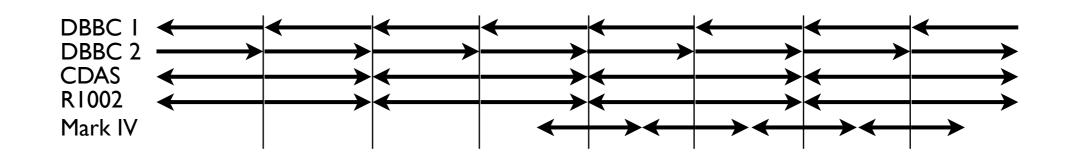
Mixed Bandwidth Correlation

- New digital backends are incompatible
- EVN: Offer 2/4 Gbit/s on selected baselines 16 MHz with 32 MHz
- Global VLBI: RDBE in DDC mode 16 MHz with 32/64 MHz
- Need to flip sidebandedness

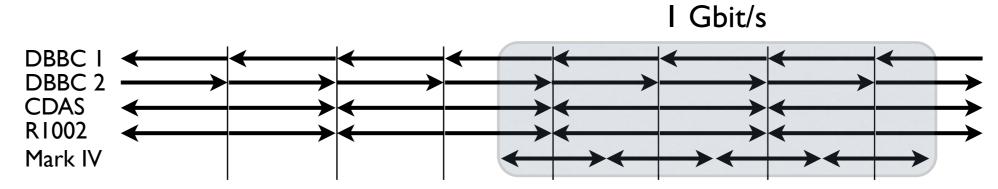




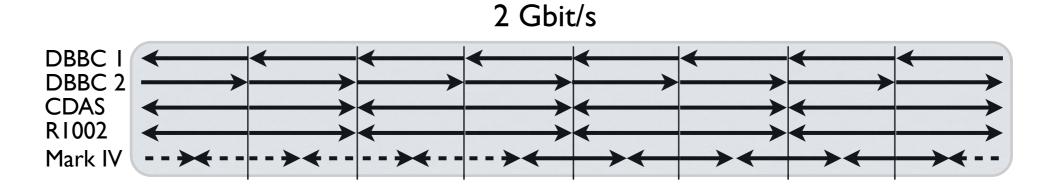
Mixed Bandwidth Correlation



Combined VEX file:



Edited VEX file (with fake 2 Gbit/s, 16 MHz station):



SFXC replaced Mark4

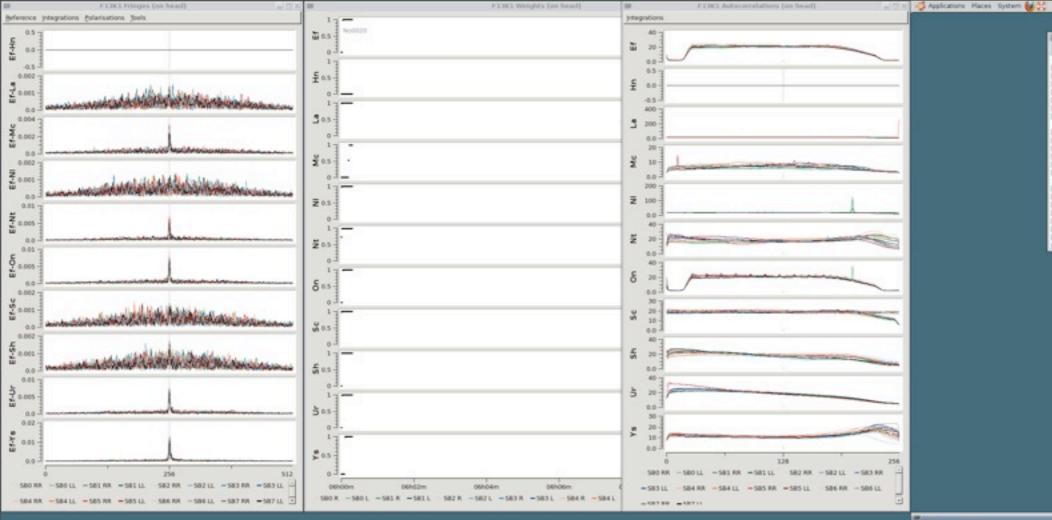
- First science project correlated in fall 2011
- SFXC primary correlator for disk since session I 2012
- All disk-VLBI on SFXC since summer 2012
- First real-time e-VLBI in december 2012
- No more Mark4 correlation ever since





Operational Tools

- Weight plot & Fringe plot
- Clock search tool
- Database integration
- Mark5 integration



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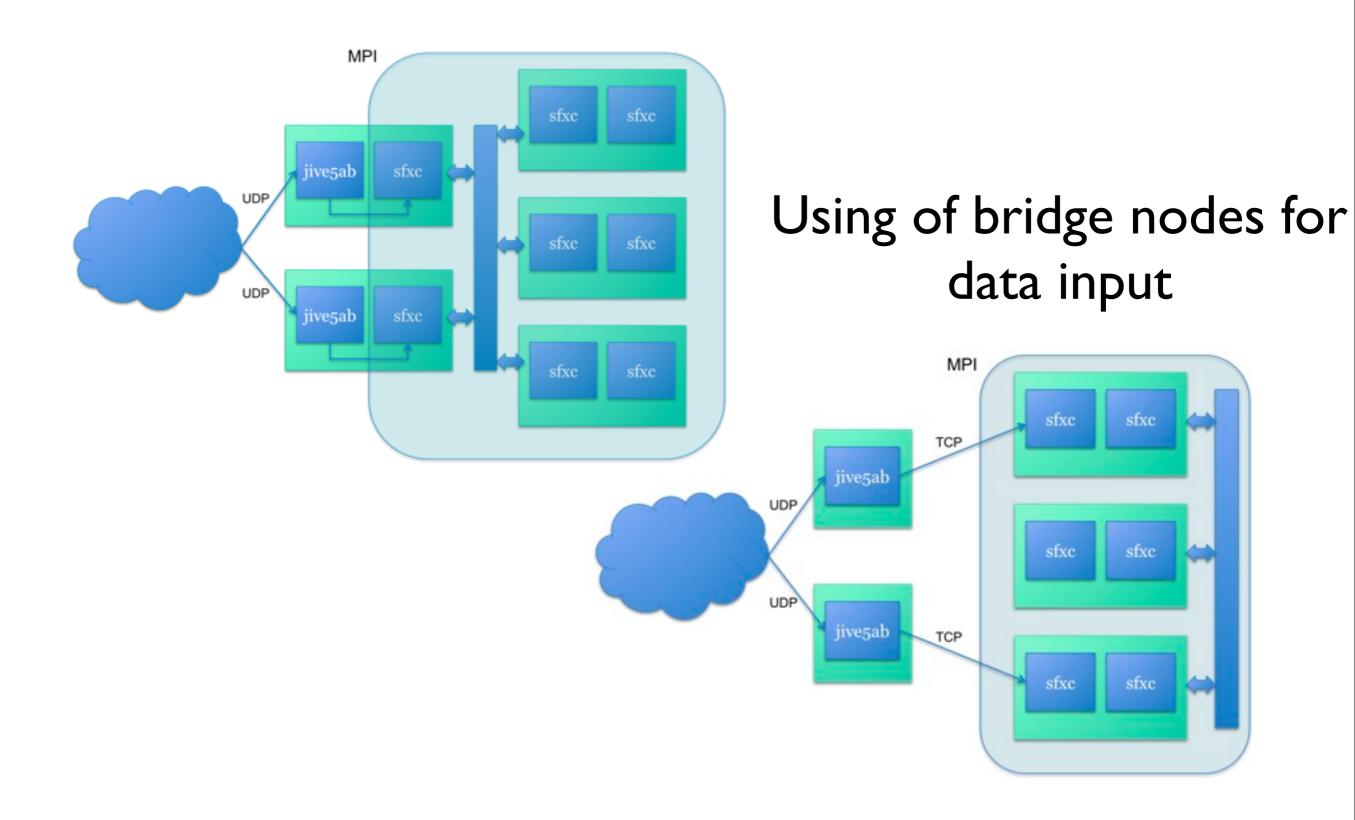
Wednesday, September 3, 2014

SFXC Hardware

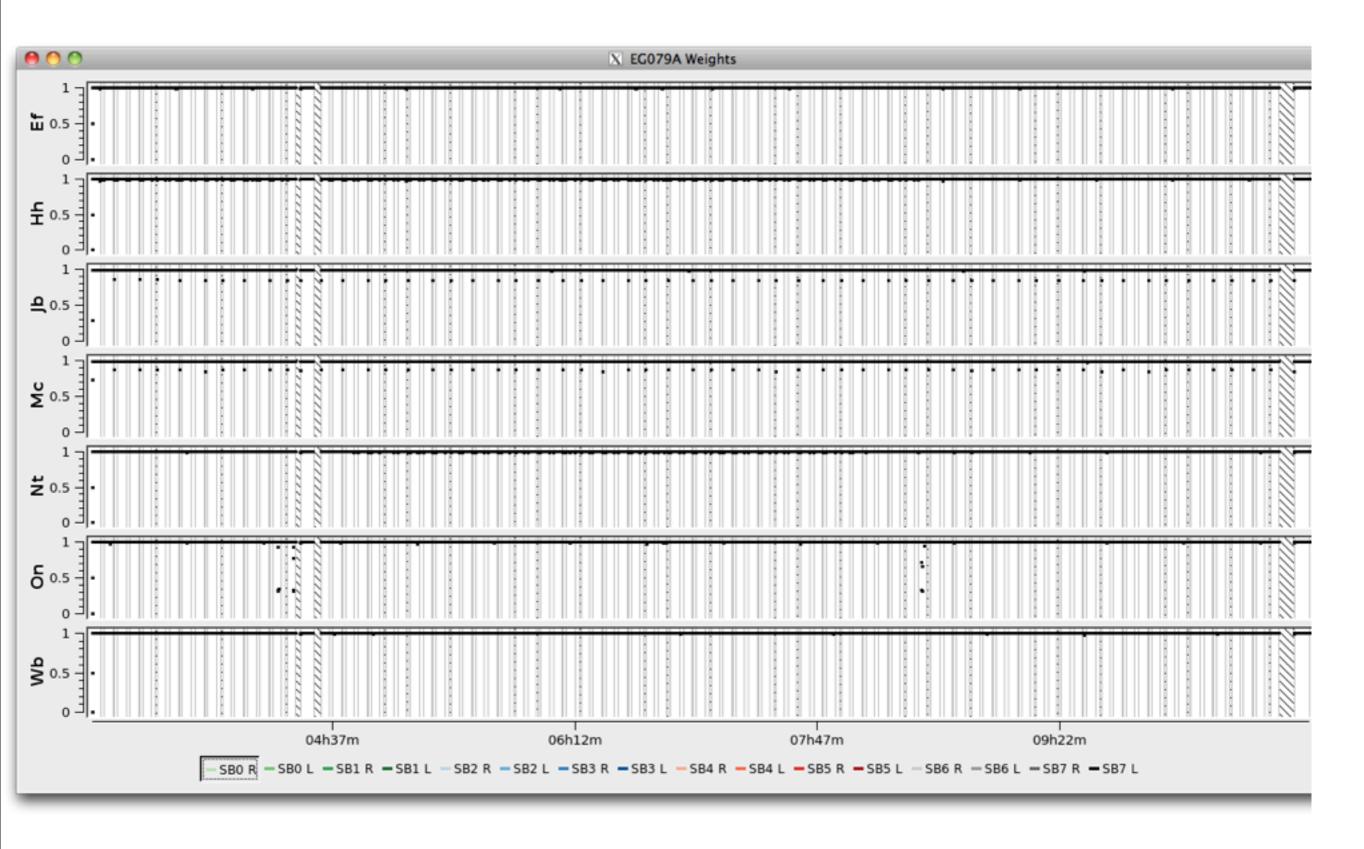
- 40 nodes; 384 cores (Intel Xeon 5500/5600/E5-2600)
- QDR Infiniband interconnect (32 Gbit/s)
- 8 nodes with 10 GbE (currently limited to 20 Gbit/s total)
- I4 stations @IGbit/s real-time (with cross-polarisations)



Real-time e-VLBI with SFXC



e-VLBI Reliability



Questions?



SFXC is Open Source software available under GPL version 2 or later Contact the speaker for details