

Correlator implementation and control meeting, Tuesday 12/1/10.  
present: Harro, Des, Arpad, Jonathan

hard- and firmware:

- The first prototype board is still expected around the end of March 2010
- Tests will take about 2 months, fully tested board available June 2010
- Production run will then start, boards expected sometime in the fall of 2010
- Before summer 2010, sufficient firmware should be in place to produce an autocorrelation or a cross correlation from (pre-) delay-corrected data
- The aim is to produce a bare-bone correlator with first "real" fringes before the end of 2010.

software:

- End of January 2010: decision on what (operating?) system to use on NIOS. Harro to discuss this with Eric
- End of January 2010: Des to provide Aard with original and VDIF-reformatted data samples, for comparison on software correlator
- June 2010: communication C-code on NIOS
- June 2010: VDIF data source available

Next meeting: Friday February 5, 11.00, my room.