

Very short JUC meeting, February 5 2015, 11.30, Arpad's room

present: Harro, Jonathan, Des, Arpad

Jonathan: found that set-batch invalidates the data valid signal halfway a packet (sometimes...). Made a fix, but it is completely clear how this can happen at all, data packets have to be delayed somehow, or signal from control system is not handled correctly, or....

Now trying to get sufficient statistics, see how often this happens, decide it is urgent/not urgent to fix on basis of this. New firmware version being compiled, still some timing errors (...), once this is done, run experiments again, finally take a look at phases and god know what other bugs.