Correlator meeting, Arpad's room, October 4 2012, 11.00 am Present: Jonathan, Des, Jintao, Harro, Salvatore, Arpad

actions:

Jonathan: test fix of Erlang performance problems reading UDP packets (on hold)

Jonathan + Salvatore: figure out how to make packet spacing adjustable (on hold)

- Des/Harro: save N11L4 scan 11 reference data from Mark5 diskpack to file

# Harro is still waiting for LTO4 tape to write data to tape. Remains - Des:write up how to deal with timestamps in the

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correlator

# done remove

-Jintao to write up latest insight in pulsar gating module.

# done remove

- Arpad: schedule PG meeting with sfxc people (Aard? or both?) discussing Jintao's three PG scenarios to comment on their

science/applicability/comparativeness to sfxc

# done, meeting held, remove

update

Jonathan: tests done, bugs found. Erlang uses 2 write to write into part of register, wreaks havoc if write is used as trigger. Fixed by Harro. Racing condition in Fifo, fixed by a wait of 16 cycles. Caused by different clock speeds of MM clock and system clock. Salvatore not convinced, but J+S will discuss further offline. Zero check is needed, in order to be able to read last 2 seconds of data from memory. Still no data ends up in files, found that destination Mac address is 0. Will be investigated.

Salvatore: changes to DDR interface on BN, synthesizes now

Jintao: investigating binary pulsar needs, see if linear will be ok on 1 second scales.

Des: working on metadata, need to enable more uniboards at a time

Harro: firmware fix (see above), ongoing fight with j2ms2, need to inspect every packet that comes out of correlator...

next meeting: October 11, after JIVE coffee, Arpad will not be there