

JUC meeting, 4 September 2014, 11.00, Arpad's room

present: Jonathan, Harro, Des, Arpad

- First priority: expanded delay coefficients (more bits). Image is ready, needs testing, needs code changes by Des. Des proposes to continue until it works, not go back to old version. Will also pass on quadratic coefficients, UB does not do anything with it yet. Unclear if needed on integrations of 1 second. Lower priority

- Des will parse VHDL list directly to get routing, to prevent future typos and misery

- Harro wonders about intrinsic phase offset or not, phase jumps that dr Bob claimed. Seems to be random phase offset at start of every scan. Phase slope steeper than SFXC, station based? Does not seem E-W or N-S related.

- splitting of data over 2 packets in case of many products should be fixed, needs testing.

- Tr scan 3 problem: Harro made fix, but behaviour of UB needs to be investigated.

- New flexbuff will be used to store experiments needed for further testing

First of all, delay coeff in proper resolution