

JUC, 3/6/2016, 11.00, Arpad's room

present Harro, Jonathan, Arpad

Jon: still has to make difference plots of correlation On-Ef with 2,4,5 and 8k frames. Look good, but should be identical of course. Needs a bit of help of Harro for this.

Putting in registers, 16 bits. Will add out-of-UDP port range as well. Registers in separate module.

Clock generator is in principle not needed. However, this is low priority, should be done maybe when moving to more uniboards.

Once convinced of correctness of handling of different packet sizes, merge all changes into 16MHz personality. Soon

Harro: changes to cpp sender, should now send all packets always, tested manually.

Need more changes to control code, cornerturned or not is not always handled properly. Harro and Jonathan will keep a better eye on progress of operationalisation, seems to be many things that are just not quite ready all the time. Talk when Des is back, see what is needed. Also talk about possible e-test next session, just see what breaks. Should of course be much simpler, but....