Correlator meeting, Arpad's room, September 27 2012, 11.00 am
Present: Jonathan, Des, Jintao, Harro, Arpad

actions:

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Jonathan: test fix of Erlang performance problems reading UDP packets
(on hold)
Jonathan + Salvatore: figure out how to make packet spacing adjustable
(on hold)
Harro: write up a small doc about timing mechanism
Des/Harro: save N11L4 scan 11 reference data from Mark5 diskpack to file
We now have two scans (scan 11 + scan 12) for 13 stations on file.
Comment Des: store converted-to-VDIF data as well?
stays but edited: Harro save raw data to LTO4 tape
Des: come up with a plan of how to deal with timestamps in the correlator
Jintao, Salvatore, Jonathan: produce a small report investigating the logic
use of the three PG scenarios and see if
they will fit or what has to be deleted to make it fit on the front node
Arpad: schedule PG meeting with sfxc people (Aard? or both?) discussing
Jintao's three PG scenarios to comment
on their science/applicability/comparativeness to sfxc.

- Harro: write up a small doc about timing mechanism
draft doc has been written, awaiting comments. remains

- Des/Harro: save N11L4 scan 11 reference data from Mark5 diskpack to
file
Harro is waiting for LTO4 tape to write data to tape. Des would like
several ref epochs in VDIF, but this is currently done automatically.
Remains

- Des: come up with a plan of how to deal with timestamps in the
correlator
Has done so, should write it all up in a memo! Action Des

- Jintao, Salvatore, Jonathan: produce a small report investigating the logic
use of the three PG scenarios and see if
they will fit or what has to be deleted to make it fit on the front node
Jonathan has looked into it, and decided that he thinks the DDR should
be used after all. Lengthy discussion, pre-loading of gate forms while
calculating phase every minute would seem best solution now. Jintao to
write this up. Action Jintao

- Arpad: schedule PG meeting with sfxc people (Aard? or both?) discussing Jintao's three PG scenarios to comment
on their science/applicability/comparativeness to sfxc
wait for Jintao document, then talk with Aard/Mark/dr. Bob? Next week.

Update by Jonathan: end of last week Salvatore has finished simulating new filterbank, all seems well. Jonathan has done the whole design on FN, also ok. Jonathan suggests to go over to new filterbank. Old design still available, but not really needed anymore. Arpad asks if this will influence the time it will take to provide Des and Harro with a working test correlator, Jonathan assures this is not the case. Nothing to be gained by sticking to old design at this point. Still needs to figure out one bug, data can be sent to UB using Harros code, but next step does not work. Ongoing. After this, test again w sine waves and take it from there. Harro asks for a byteswap to be implemented on BN as he has to byteswap, inspect, re-byteswap. Jonathan will look into this.