JUC, 22-10-2015, 15.00, Arpad's room

present Jonathan, Harro, Des, Arpad

Des: comparing the averaging strategies shows that FPGA implementation must be wrong. Harro wrote integer version, compares within noise with floating point averaging in j2ms2.

Jonathan: BP shape left, maybe missing FFT in integration? Somewhat mysterious, would have seen missing products (surely?). Des will check on hardware, see if same result as simulation.

Des: this exercise showed up some loose ends like software living in people's home dirs, wrong versions. Had to use Aribox instead of FlexBuff, need some fixes.
Made Erlang fail if app-reset times out. This happens surprisingly often. Some discussion on why, network problem? Unictl directly connected to switch on first UB, from there to second. Two switches. If we are using the second right now, not clear. Des will look into this some more.

Some discussion about creating a diagnostic tool to test all the outputs, all products. Which we have never done yet. Should be easy to turn all outputs on in a hack, either clone data and fill all input nodes, or send data to only few nodes, check where they end up. Will be some more thinking about this. such a tool would be immensely useful