JUC meeting, 19-06-2014, Arpad's room

present Jonathan, Harro, Des, Guifre, Arpad

Jonathan: fifo full signal not there: wrong bit was being read, unclear why, but no problem, all works fine now

Checking fractional delay, looking at simulations. Thought was off by 1 clock, fixed that, but not sure now. Found other problem, fifo not getting reset, overflowing. Would cause fractional dely from 16 FFTs earlier to be applied. Replaced fifo with registers, not tested yet, this afternoon will know. May have to remove fix of one clock again. Found other problem, in that re-clock of signal failed, this seems to be a modelsim problem however. May be more occurrences, is looking at this now.

Des: all seems to be working, fixed problem with data sender, Harro made script to stop and start JUC control system, basically ready to go. Although Harro still has to distribute his data sender to all Mark5s, will make debian package and use puppet (or have Paul do it). Des will also show Guifre how to use the new system...

Guifre will run SFXC with exact time stamps, Harro will check if they are exactly equal to UB time stamps. Tomorrow: whether new firmware available or not, run N14C1 completely, smooth down to SFXC resolution, operational test. One scan should be done at high resolution, also with SFXC, for detailed comparison. Low resolution results with SFXC are available. Make comparisons. Des to provide Dima and Sergei with more data for phase noise comparison, after fix of Jonathan.