JUC, 17-03-2016, 11.00, Arpad's room

present Des, Jonathan, Arpad

Jonathan: wanted to fix configuration address in 32MHz version, but SOPC builder did not work anymore. Henk managed to fix it (or was it Jan?), now done, recompiling. This will need a new system.h file. All under SVN.
Per station frame size. Register with values, now is defaulted. Needs some Erlang help, which Des will provide. Eight values per node. Test: did not work. Needs to change one of the checks. In September 2 stations at 5000, and then at 8000, which worked, now test with mixed sizes.
UB2: DDR testing a bit at a standstill, Leon is ill, but long-term DDR test worked over one weekend, all seems to be pretty much ok.
Still has to look at the effort needed to make mixed experiments possible (definition of what bands), whether it should be done in FW or SW (Des and Arpad think SW).

Des: 32MHz data has been saved to flex0, 2 times 10 minutes, from 2 different scans. Need corner turning, have to discuss with Harro.
Test with correlator and control SW: forgot model generation, takes ~20min, might be worth looking into parallelizing this. c++ data reader is in /opt, which is not in the path. Tell Wybren to fix it for all units. Mark5C in vex, not supported yet, took station out, works. Will not test with operators. Spectral averaging needs to be default.

Afterwards: Des found out that 32 freq points can be selected, and it works. Has turned a long job on, see if UB goes up in smoke.