

JUC, 16/6/2016, 11.15, Arpad's room

present: Harro, Des, Jonathan, Arpad

Des: control system up and running, everything seems to be working fine, data on Mark5, flexbuff, you name it. Only pre-cornerturned VDIF will need some patches. This is of course needed for e-VLBI.

Occasional quirks with jive5ab, possibly interference with e-shipping.

However, new version of jive5ab may have fixed whatever triggered this problem.

After discussion with Mark, SFXC can be used for model generation, except during e-VLBI. This will need some modifications etc, nothing very complicated.

Arpad: next e will be this month, not realistic, after that September 21. Should be possible

Harro: list of to-do's:

- synchronizer needs changes, next UT sec?
- need configfila10G in addition to config5ab
- tell UB to ignore sequence numbers
- skip starting up VDIF senders
- mix fila10G and mk5, should be possible

Jonathan: made new image 16MHz with new features, mixed frame size and registers. Ran 15 seconds of data through 2 UBs with different images, looks quite the same (which it really should be).

Found that system.h is very very old (2012). Need new one, always possible to go back to old one.

First test new image with 5k frames, then try mixed frames.

Harro: how about mixing 8 and 16 MHz data? Consensus is that this should be possible, like mixing 16 and 32. Not sure this will ever be exercised in the real world though.

Des to make estimate of effort needed in to-do list, make timeline.