JUC, 16-4-2015, 11.00, Arpad's room

present: Harro, Des, Jonathan, Arpad

Jonathan: tried to slow down clock (suggestion by Sergei). Broke.

Some discussion. Bit comparison with results of simulation might be worthwhile. Takes a lot of time. Maybe over the weekend.

32MHz: problems remaining. Control code is not ready yet. Routoing FN BN still a problem. Des is in process of removing hard coded stuff (gooood...)

Design UB2 test firmware: transceiver test module is very big, will not fit. Strange, as Jonathan's code is much (factor 10?) smaller. Will discuss with Daniel at local meeting.