JUC, 10-09-2015, 13.30, Arpad's room

present: Jonathan, Harro, Des, Arpad

Jonathan: 32MHz meets timing! Added pipelining in validity calculation, but need to check, may have broken all kind of stuff. HW test should flush out potential problems

Upgraded the control code, nothing works anymore. Des will fix it, should be ok real soon

Des: problem with different control codes fixed, small difference in calculation of coefficients, as suspected. He also ported the model generation to unictl, use simple make file now, used to use SFXC method. Unictl will be upgraded with larger disk and new ubuntu after Paul returns, needs escalating though.