

JUC meeting, 10-4-2014, 11.00, Arpads room

present : Jonathan, Harro, Des, Guifre, Arpad

Jonathan: subband bug under investigation. Found the phase is right once per second, wrong in between. Thinks he found the problem in module written by Jintao, which normally does not get synthesized into the simulations. Delay is one 32 bit word, phase is two words, this seems to be treated as one word instead. Something seems wrong with station numbers too, which is weird as the fact that we get fringes indicates that they should be right. Under investigation.

UB2: got some of the pinning right, managed to run it with several memory interfaces etc. However, key pins are put in manually, Quartus decides on the others, but the numbering is impossible to understand, so the checking is somewhat impossible. Reported this to Karl.

Salvatore (ill) has been updating the doc for packetRX write part, started read doc

Des: correlation code does not properly report when correlation finished, so have to put in manual time out. This is highly inaccurate and problem for Guifre. It all should work, will start debugging. Harro and Des puzzled.

Guifre: ran SFXC with VDIF data, but quite a lot of handwork needed to map VDIF threads. Des has old offline tool and new inline tool with algorithms to do just this, could give output in vex format, save Guifre loads of work. Will provide this to Guifre asap. Guifre compared data at 0.5s, 100s run, SFXC and UB, not really same results although looking much better than with 0.25s integration. He also will try to run expt from Mark5 disk packs, just to exercise the system, not for comparison.