JUC, 8-2-2018, 11.00

present Harro, Des, Benito, Jonathan, Arpad

Des: fixed the bad timestamp fall-over of control code
Benito: did not do anything, there was e, will give it a go next week
Harro: has had contact with Ed, new FS soon, dropped packets will be visible
Jonathan: will place 32 MHz versions on both UBs

Next steps:

test 1000 and 2000 B packets, to many nodes, see when it seizes up, can be done in-house.
Toolchain needs to be fixed, we suffered of packet loss last times
Harro will provide non-lossy squirter
Jonathan: signal tap in DDR controller, can be turned on
Need a simulation, which should be one day work about.

After BHCam stuff done, and after Benito has made an image, Des will start synchronising recorded and e versions of control code