

JUC, 4/2/2016, 11.30, Arpad's room

present Jonathan, Harro, Des, Arpad

Jon: possible reason found for bumps in cross correlations, at input to
DDR: new improved version of clock offset insertion module, not tested
yet.

Temp fix: better, but not quite, bumps gone, but decreasing bp + more
noise

Des: magic nr 11 in delay correction had to be replaced by 12 (size of
FFT), is now variable. Being tested.